



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein, et al.

Title: MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND LOGIC BLOCK STRUCTURE AND METHOD THEREFOR

Application No.: 10/675,212 Filed: September 30, 2003

Examiner: Ho, Hoai V. Group Art Unit: 2818

Atty. Docket No.: 023-0024 Confirmation No.: 9946

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**INFORMATION DISCLOSURE STATEMENT
37 C.F.R. § 1.97(c)**

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- Form(s) PTO/SB/08A (8 page(s), with copy of 2 reference(s)).
- Form(s) PTO/SB/08B (1 page(s), with copy of 10 reference(s)).
- Listing of Pending Applications (1 page(s), with copy of no application(s)).
- Other: N/A

Citation of such information shall not be construed as (i) an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention, (ii) a representation that a search has been made, other than as described herein, or (iii) an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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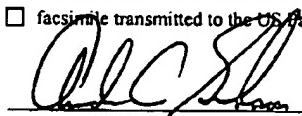
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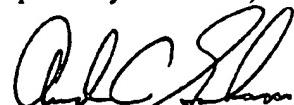
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3-16-05
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Respectfully submitted,



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